

| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|-------|-------|--|---|------------------|---------|------------------|
| L1 | 12053 | virtual near component and behavior and identif\$6 and schedul\$6 and allocation and partition\$5 architec\$6 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2004/11/30 09:47 |
| L2 | 238 | virtual and component and behavior and identif\$6 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2004/11/30 09:44 |
| L3 | 0 | virtual near component and behavior and identif\$6 and schedul\$6 and CFSM | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2004/11/30 09:45 |
| L4 | 117 | virtual near component and behavior and identif\$6 and schedul\$6 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2004/11/30 09:46 |
| L5 | 71 | virtual near component and behavior and identif\$6 and schedul\$6 and allocation | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2004/11/30 09:46 |
| L6 | 49 | virtual near component and behavior and identif\$6 and schedul\$6 and allocation and partition\$5 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2004/11/30 09:46 |
| L7 | 47 | virtual near component and behavior and identif\$6 and schedul\$6 and allocation and partition\$5 and architec\$6 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2004/11/30 09:47 |
| L8 | 6 | virtual near component and behavior and identif\$6 and schedul\$6 and allocation and partition\$5 and architec\$6 and preemption | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2004/11/30 09:48 |
| S1 | 66 | Cadence and VCC and @ad < "20001017" | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2004/03/29 14:26 |

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| S2 | 66 | Cadence and (VCC "Virtual Component Codesign") and @ad < "20001017" | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2004/03/29 14:25 |
| S3 | 0 | Cadence and "Virtual Component Codesign" and @ad < "20001017" | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2004/03/29 14:26 |
| S4 | 0 | Cadence and "Virtual Component Codesign" | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2004/03/29 14:26 |
| S5 | 0 | "Virtual Component Codesign" | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2004/03/29 14:26 |
| S6 | 3151 | Cadence and @ad < "20001017" | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2004/03/29 14:26 |
| S7 | 91 | Cadence and @ad < "20001017" and "system level" | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2004/03/29 14:27 |
| S8 | 3 | Cadence and @ad < "20001017" and "system level" and "integration system" | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2004/03/29 14:27 |
| S9 | 3 | Cadence and @ad < "20001017" and "system level simulation" | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2004/03/29 14:28 |
| S10 | 3 | Cadence and @ad < "20001017" and ("system level simulation" "system level simulating") | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2004/03/29 14:29 |
| S11 | 3 | "Cadence Design Systems" and @ad < "20001017" and ("system level simulation" "system level simulating") | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2004/03/29 14:31 |

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| S12 | 3 | "Cadence Design Systems" and @ad < "20001017" and ("system level" "system-level") adj (simulation simulating simulator) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2004/03/29 14:33 |
| S13 | 3 | "Cadence Design Systems" and @ad < "20001017" and ("system level" "system-level") adj (simulation simulating simulator) and (system cadence level simulation simulating simulator simulate model behavior capturing capture captured simulated mapping architecture wire connection component element schedule schedulable scheduler scheduling identifying identification identified message messaging messages reaction step hierarchical embedded embed consumer) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2004/03/29 14:40 |